

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Lap-Wai CHOW, et al.

RE: Response to Notice to File Corrected Application Papers (Notice of Allowance Mailed)

Serial No.: 09/768,904

Filed: January 24, 2001

Art Unit: 2815

For: "INTEGRATED CIRCUITS  
PROTECTED AGAINST REVERSE  
ENGINEERING AND METHOD FOR..."

Examiner: Nguyen, Joseph H.

Our Ref.: B-3964 618029-8

Date of this paper: September 27, 2007

**RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS  
(NOTICE OF ALLOWANCE MAILED)**

Sir:

In response to the Notice to File Corrected Application Papers (Notice of Allowance Mailed) dated 08/21/2007, wherein Figure 2 is identified as deficient, a replacement sheet of formal drawings (Fig. 2) is enclosed herewith for the above-identified application.

The Commissioner is authorized to charge any additional fees which may be required or credit overpayment to Deposit Account No. 12 0415 and, in particular, if this paper is not timely filed, then the Commissioner is authorized to treat this paper as including a petition to extend the time period pursuant to 37 CFR 1.136(A) requesting an extension of time of the number of months necessary to make this paper timely filed and the petition fee due in connection therewith may be charged to Deposit Account No. 12 0415.

/Alexander Krayner 60854/

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Alexander Krayner  
Attorney for the Applicant  
Registration No.: 60,854

c/o      LADAS & PARRY LLP  
**(Customer No. 36716)**  
5670 Wilshire Blvd., Suite 2100  
Los Angeles, CA 90036-5679  
Telephone No.: (323) 934-2300  
Facsimile No.: (323) 934-0202

Encl.: 1 Replacement Sheet of drawings(Fig. 2)

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**CERTIFICATE OF TRANSMISSION**

*I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted electronically to the United States Patent and Trademark Office on September 27, 2007.*